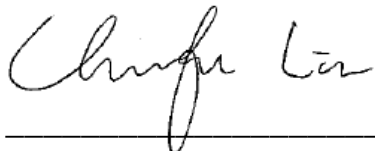
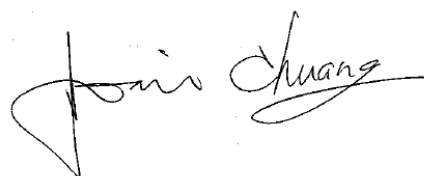


Harvatek Surface Mount CHIP LEDs Approval Sheet
Model No.: HT-F118NG

Acknowledged by



Section Manager
Production Engineering Dept.



Manager
Production Engineering Dept.

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Introduction

- *The information contained herein is presented only as a guide for the applications of our products. No responsibility is assumed by HARVATEK for any infringements of intellectual property or other rights of the third parties which may result from it use.*
- *Harvatek is continually effort to improve the quality of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing HARVATEK products, to comply with the standards of safety in making a safe design for the entire system, and to avoid situations in which a malfunction or failure of such HARATEK products cause loss of human life, bodily injury or damage to property.*
- *The HARVATEK products listed in this document are intended for usage in general electronics (computer, personal equipment, office equipment, industrial robotics, domestic, etc...) These products are neither intended nor warranted for usage in equipment that requires extraordinarily high quality and/or reliability or a malfunction or failure of which may cause loss of human life or bodily injury.*
- *In developing your designs, please ensure that HARVATEK products are used within specified operating ranges as set forth in the most recent HARVATEK products specifications.*
- *Also, please keep in mind the precautions listed in this document.*

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Product Specification

	Specification	Material	Quantity
Iv	180-450mcd @20mA/ Ta= 25° C Tolerance: ± 10%		
Wavelength	515-535nmnm @20mA/ Ta= 25° C		
Vf	2.7~3.9 (0.2V/1Bin) @20mA/ Ta= 25° C		
Ir	< 100 µA @ V _R = 5 V		
Resin	Milky white	Epoxy resin	
Carrier tape	According to EIA 481-1A specs	Conductive black tape	2000pcs per reel
Reel	According to EIA 481-1A specs	Conductive black	
Label	HT standard	Paper	
Packing bag	220x240mm	Aluminum laminated bag/ no-zipper	One reel one bag
Carton	HT standard	Paper	Non-specified

Others:

Every mid-box will be loaded 5 reels. These 5 reels can be different in lot, Iv, lambda, or Vf. Every reel will have an independent label to identify its specification and the mid-box there will have a corresponding label post on it.

ATTENTION: Electricstatic Discharge (ESD) protection




The symbol shown on the page herein to introduce 'Electro-Optical Characteristics'. ESD protection for GaP and AlGaAs based chips is still necessary even though they are safe in low static-electric discharge. Parts built with AlInGaP, GaN, or/and InGaN based chips are **STATIC SENSITIVE devices**. ESD protection has to be considered and taken in the initial design stage.

If manual work/process is needed, please ensure the device is well protected from ESD during all the process.

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Label Spec.:

HARVATEK		Date: yyyy/mm/dd
CUSTOMER P/N: 		
HARVATEK P/N: 	QTY: PCS 	
LOT NO: 	QC	
IV BIN: COLOR BIN: VF:		

■ Harvatek P/N

H T - F 1 1 8 NG



Series Name	Emitting Color
HT-F118: 3.2x1.2x0.8mm	NG: Green@20mA

■ Lot No.

1 2 3 4 5 6 7 8 9 10
P 1 2 2 3 0 A - D T

Code 1	Code 2	Code 3	Code 4, 5	Code 6, 7	Code 9	Code 10
	Mfg. Year	Mfg. Month	Mfg. Date	Lots	Resin Color	Packaging
Internal Tracing Code	Z: 2000 1: 2001 2: 2002 3: 2003	1: Jan. 2: Feb. 9: Sep. A: Oct. B: Nov. C: Dec.	1~31/ (30)	01~99, A,B,C...	D: Milky White	T: Taped Reel

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■ Iv Bin:

Color	Bin Code	Spec. Range
Green	S1	180-230mcd
	S2	230-285mcd
	T1	285-360mcd
	T2	360-450mcd

■ Color Bin:

Color	Bin Code	Spec. Range
Green	A	515-520nm
	B	520-525nm
	C	525-530nm
	D	530-535nm

■ Vf Bin:

Color	Bin Code	Spec. Range
Green	G8	2.7-2.9V
	H7	2.9-3.1V
	H8	3.1-3.3V
	J7	3.3-3.5V
	J8	3.5-3.7V
	K7	3.7-3.9V

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Product Feature

Electro-Optical Characteristics

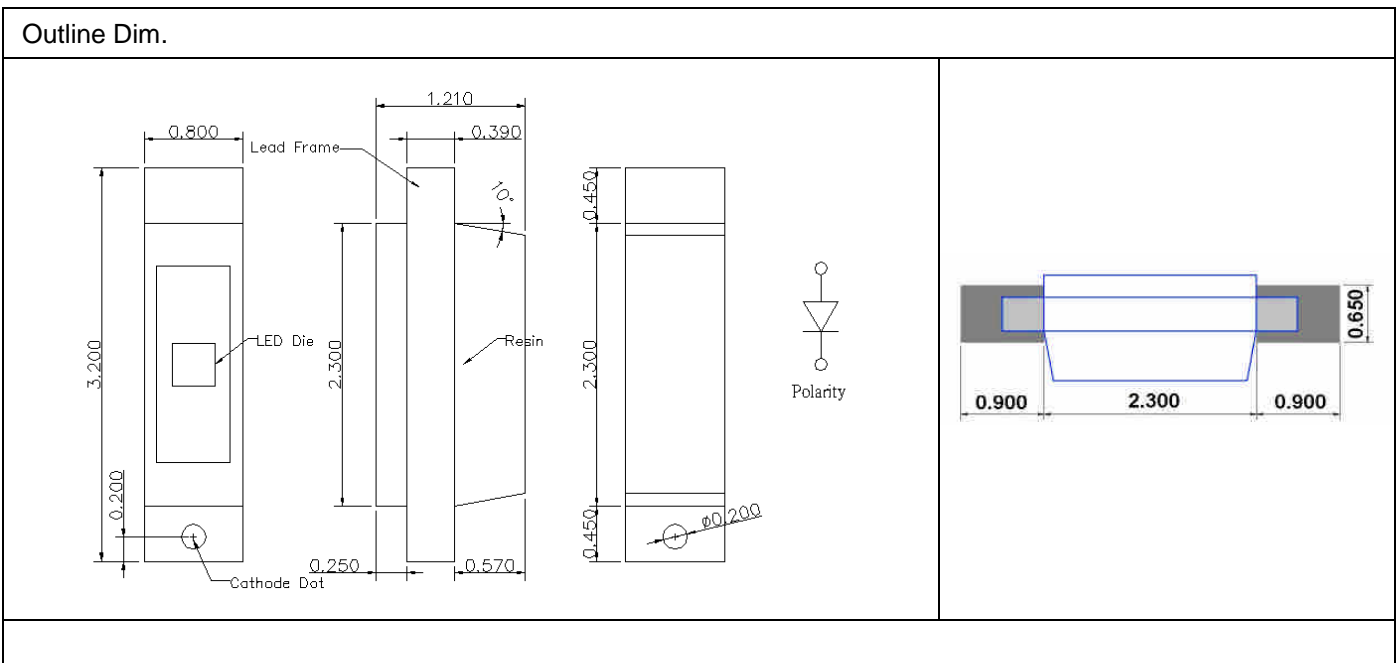
(I_F @ 20mA, T_a 25 °C)

Code for parts	Lighting Color	Material	V_F (V)		λ (nm)			I_V (mcd)
			typ	max	λ_D	λ_P	$\Delta\lambda$	Typical.
HT-F118NG	Green	InGaN	3.3	3.9	527	520	40	280

Package Outline Dimension and Recommended Soldering Pattern for Reflow Soldering

Unit: mm Tolerance: +/-0.1

Outline Dim.



Absolute Maximum Ratings

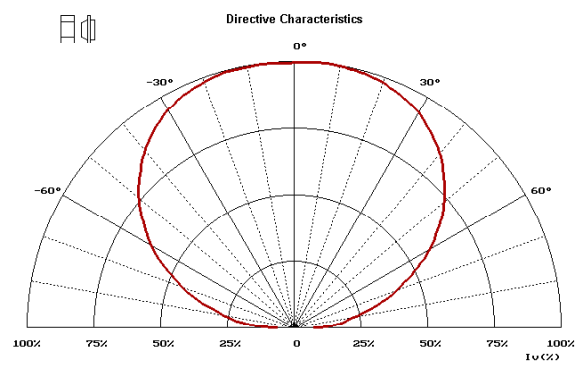
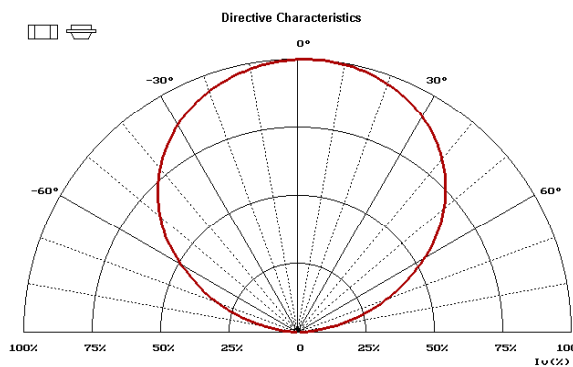
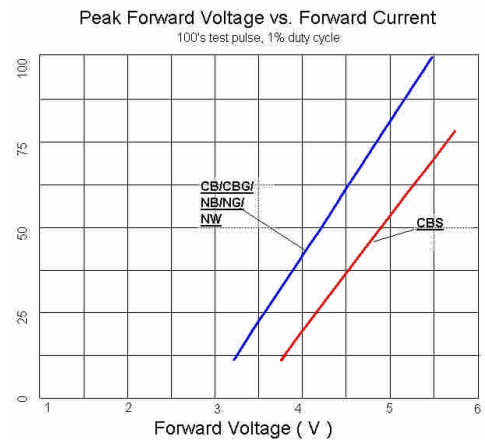
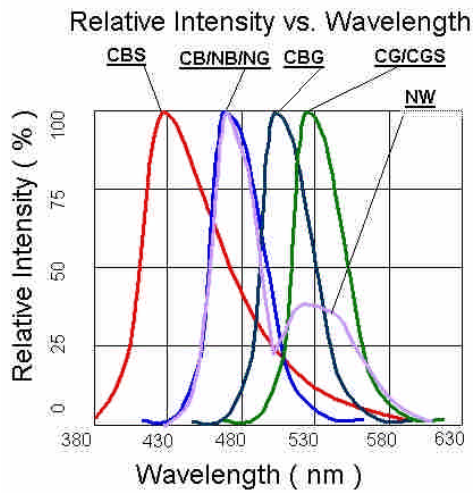
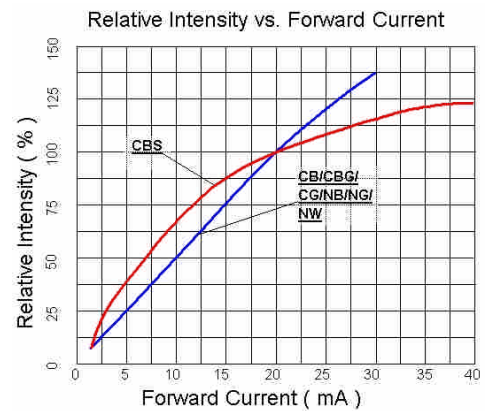
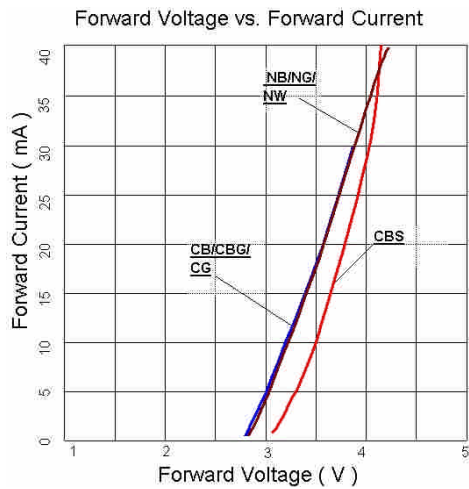
(T_a 25 °C)

Series	P_d (mW)	I_F (mA)	I_{FP} (mA)	V_R (V)	I_R (uA)	T_{OP} (°C)	T_{ST} (°C)
HT-F118NG	78	20	80**	5	<100@ $V_R = 5$	-30~+80	-40~+85

** Condition for I_{FP} is pulse of 1/10 duty and 0.1msec width

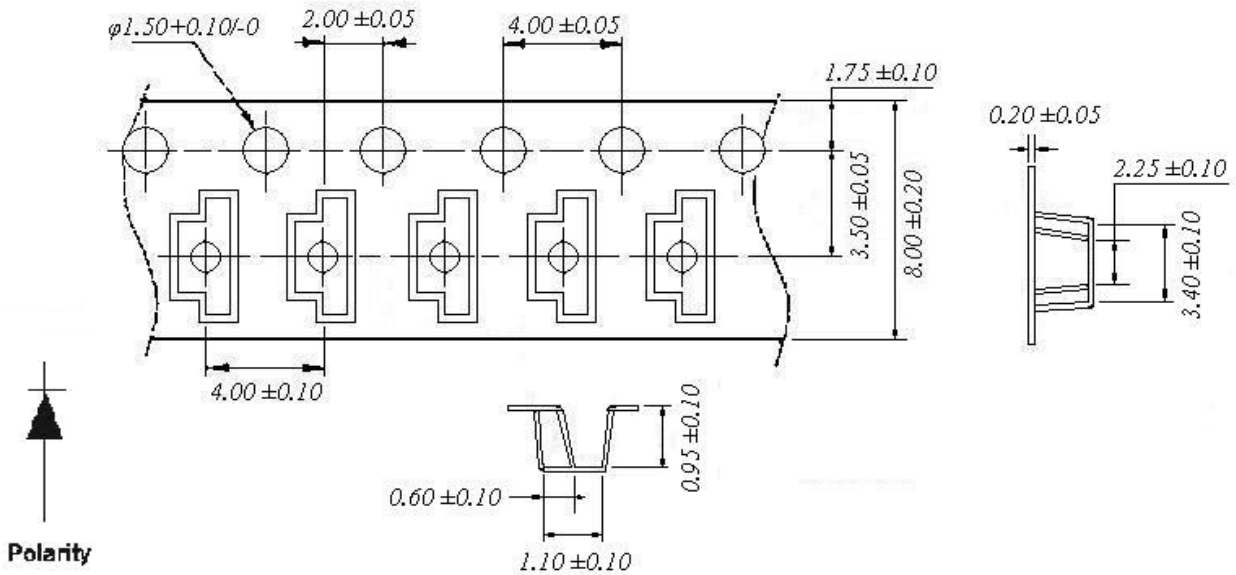
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Characteristics of HT-F118 Series



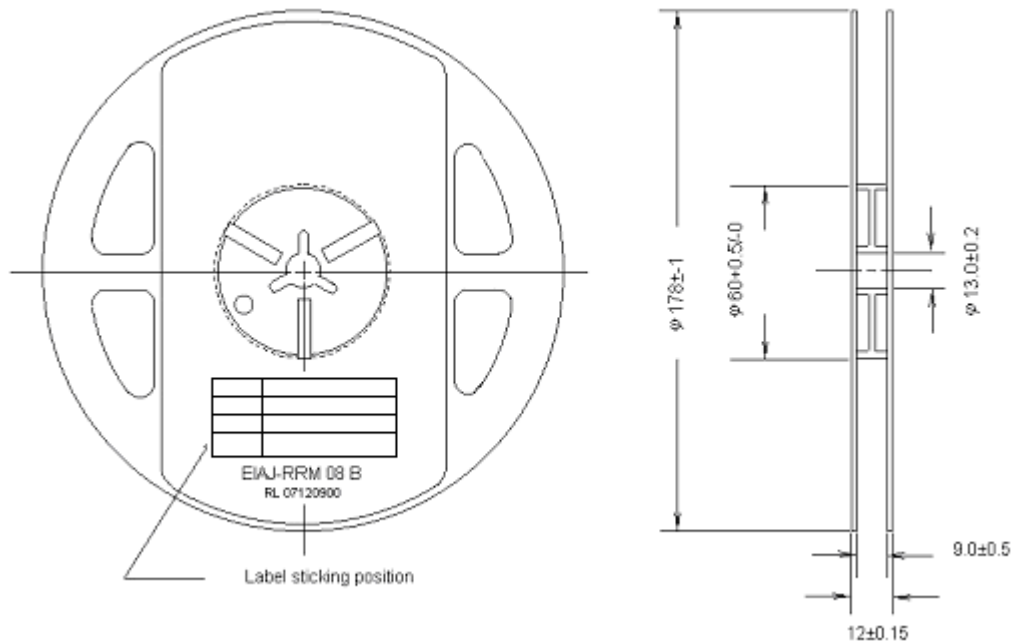
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Packaging Tape, Reel, and Packing Model Tape Dimension



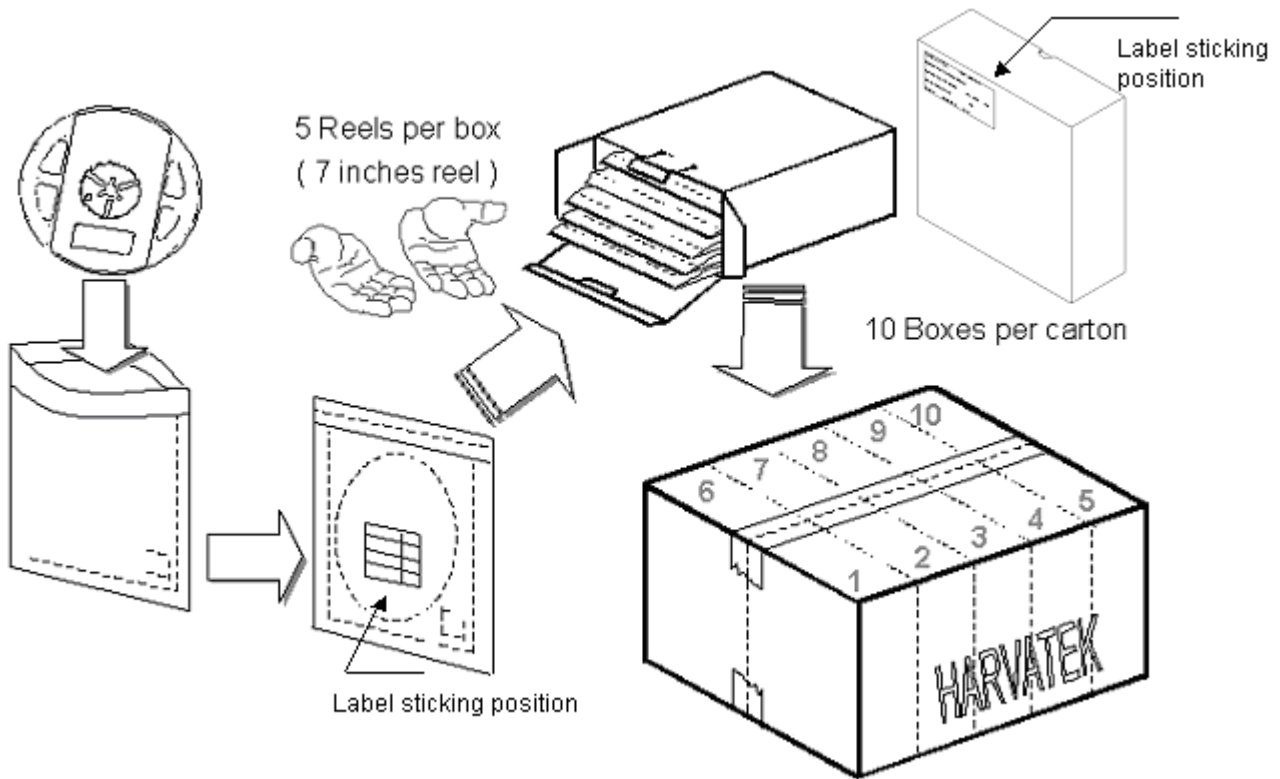
Unit: mm Tolerance: + / - 0.1 mm

Reel Dimension



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Packing Model



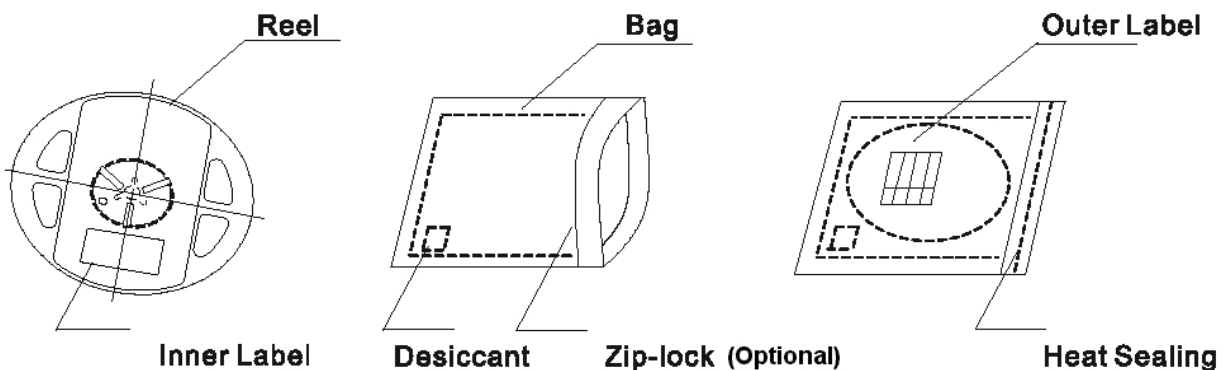
5 boxes per carton is available according to shipping quantity.

Dry Pack

Any SMD optical device, like this chip LED, is **MOISTURE SENSITIVE device**. Avoid absorbing moisture at any time during transportation or storage. Every reel will be packaged in the moisture barrier anti-static bag (Specific bag material will depend upon customers' requirement or option). And the bag is well sealed before shipment.

By customer's requirement, we will put a humidity indicator in each moisture barrier anti-static bag before shipment.

The package is the following:



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Cautions of Pick and Place

It should be avoided to load stress on the resin during high temperature.

Avoid rubbing or scraping the resin by any object.

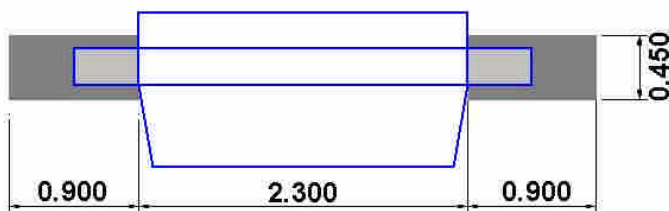
Electric-static may cause damage to the component. Please confirm that the equipment grounding well. Using an ionizer fan is recommended.

PRECAUTIONS

1. Avoid absorbing moisture at any time during transportation or storage.
2. Anti-Static process is needed especially when handling GaN, InGaN, and AlInGaP products.
3. It is suggested to connect the unit with a proper series current limit resistor. Avoid driving reverse voltage over the specification of LEDs when turning the unit ON/OFF.
4. Any application should refer to the specifications of absolute maximum ratings.
5. Avoid any direct contact with the viewing area.
6. If possible, assemble the unit in a clean room or dust-free environment.

Soldering pattern

The dimensions of the recommended soldering pattern may not meet every user. Please confirm and study first before designing the soldering pattern in order to obtain the best performance of soldering. Recommended soldering pattern is listed below.



Unit: mm

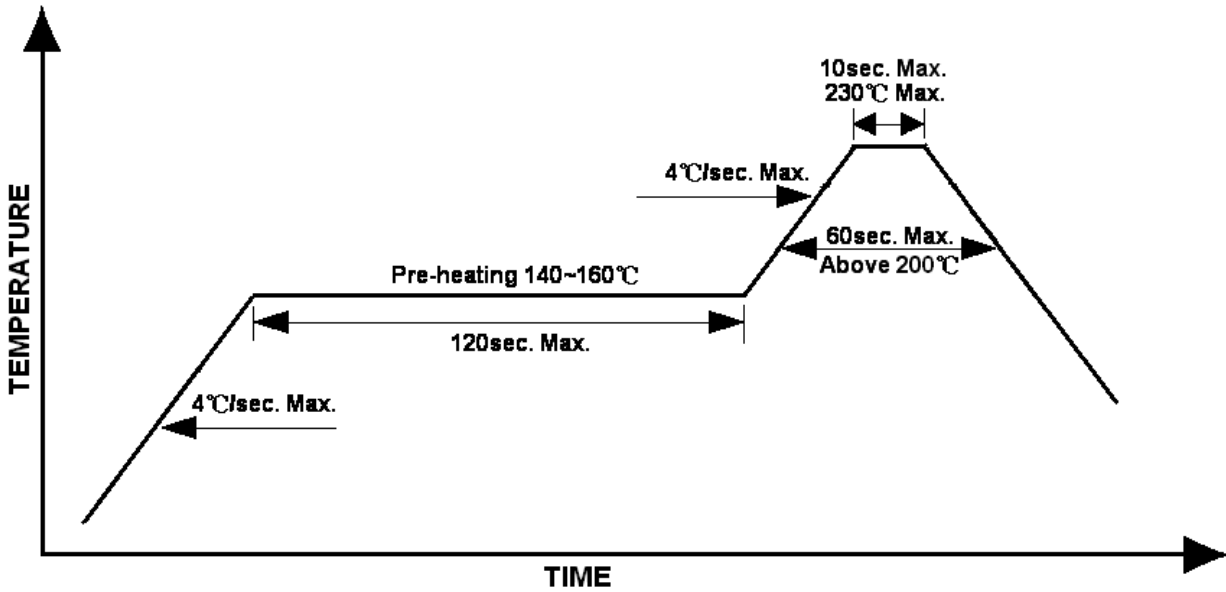
Soldering terminal may shift in x, y direction.

Reflow Soldering

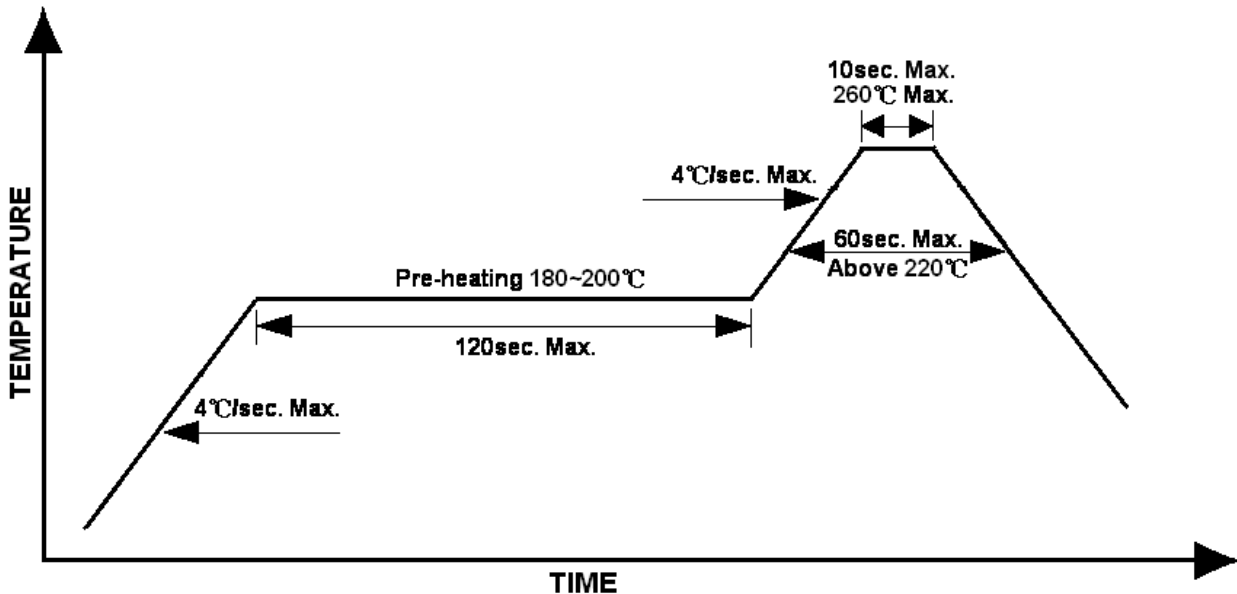
- ◆ Recommend tin glue specifications:
Melting temperature: 178~192 °C
- ◆ Never take next process until the component is cooled down to room temperature after reflow.
- ◆ The recommended reflow soldering profile (measuring on the surface of the LED resin) is following:

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Lead Solder



Lead-free Solder



Rework

- ◆ Customer must finish rework within 5 sec. under 260 °C.
- ◆ The head of iron cannot touch copper foil.
- ◆ Twin-head type is preferred.

Cleaning

The conditions of cleaning after soldering:

An alcohol-based solvent such as isopropyl alcohol (IPA) is recommended.

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Temperature×Time: <50 °C×30sec, or <30 °C×3min

Ultra sonic cleaning: < 15W/ bath; Bath volume: 1liter max.

Curing: 100 °C max, <3min

Do not contact with component on the assembly board.

Cautions of Pick and Place

It should be avoided to load stress on the resin during high temperature.

Avoid rubbing or scraping the resin by any object.

Electric-static may cause damage to the component. Please confirm that the equipment grounding well. Using an ionizer fan is recommended.

Reliability Test

Item	Frequency/ lots/ samples/ failures	Standards Reference	Conditions
Precondition	For all reliability monitoring tests according to JEDEC Level 2	J-STD-020	1.) Baking at 85°C for 24hrs 2.) Moisture storage at 85°C/ 60% R.H. for 168hrs
Solderability	1Q/ 1/ 22/ 0	JESD22-B102-B And CNS-5068	Accelerated aging 155°C/ 24hrs Tinning speed: 2.5±0.5cm/s Tinning: A: 215°C/ 3±1s or B: 260°C/ 10±1s
Resistance to soldering heat		CNS-5067	Dipping soldering terminal only Soldering bath temperature A: 260±/-5°C; 10±/-1s B: 350±/-10°C; 3±/-0.5s
Operating life test	1Q/ 1/ 40/ 0	CNS-11829	1.) Precondition: 85°C baking for 24hrs 85°C/ 60%R.H. for 168hrs 2.) T _{amb} 25°C; I _F =20mA; duration 1000hrs
High humidity, high temperature bias	1Q/ 1/ 45/ 0	JESD-A101-B	T _{amb} : 85°C Humidity: 85% R.H., I _F =5mA Duration: 1000hrs
High temperature bias	1Q/ 1/ 20	HT specs.	T _{amb} : 55°C I _F =20mA Duration: 1000hrs
Pulse life test	1Q/ 1/ 40/ 0		T _{amb} 25°C, I _F =20mA,, I _p =100mA, Duty cycle=0.125 (tp=125 μs, T=1sec) Duration 500hrs)
Temperature cycle	1Q/ 1/ 76/ 0	JESD-A104-A IEC 68-2-14, Nb	A cycle: -40 degree C 15min; +85 degree C 15min Thermal steady within 5 min.. 300 cycles 2 chamber/ Air-to-air type
High humidity storage test	1Q/ 1/ 40/ 0	CNS-6117	60±3°C 90+5/-10% R.H. for 500hrs
High temperature storage test	1Q/ 1/ 40/ 0	CNS-554	100±10°C for 500hrs
Low temperature storage test	1Q/ 1/ 40/ 0	CNS-6118	-40±5°C for 500hrs

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