

Harvatek Surface Mount CHIP LEDs Approval Sheet Model No.: HT-110BP

Acknowledged by

Section Manager

Production Engineering Dept.

Manager

Production Engineering Dept.

	Official Product	ial Product HT Part No. HT-110BP Your Part No.		Data Sheet No.	
	Tentative Product	entative Product ************************************		HDS-110-K144	
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Introduction

- The information contained herein is presented only as a guide for the applications of our products. No responsibility is assumed by HARVATEK for any infringements of intellectual property or other rights of the third parties which may result from it use.
- Harvatek is continually effort to improve the quality of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing HARVATEK products, to comply with the standards of safety in making a safe design for the entire system, and to avoid situations in which a malfunction or failure of such HARATEK products cause loss of human life, bodily injury or damage to property.
- The HARVATEK products listed in this document are intended for usage in general electronics (computer, personal equipment, office equipment, industrial robotics, domestic, etc...) These products are neither intended nor warranted for usage in equipment that requires extraordinarily high quality and/or reliability or a malfunction or failure of which may cause loss of human life or bodily injury.
- In developing your designs, please ensure that HARVATEK products are used within specified operating ranges as set forth in the most recent HARVATEK products specifications.
- Also, please keep in mind the precautions listed in this document.

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Product Specification

	Specification	Material	Quantity
lv	45-180mcd		
	@20mA/ Ta= 25 ^o C		
	Tolerance: + 10%		
Chromaticity	C1: X-0.25~0.27 Y-0.2325~0.2775		
Coordinates	C2: X-0.27~0.29 Y-0.26~0.305		
	D1: X:0.29-0.31, Y:0.2875-0.3325		
	D2: X:0.31-0.33, Y:0.315-0.36		
	@20mA/ Ta= 25 ^o C		
Vf	2.7-3.9v		
	@5mA/ Ta= 25 ^o C		
Ir	< 100 μA @ V _R = 5 V		
Resin		Epoxy resin	
Carrier tape	According to EIA 481-1A specs	Conductive black tape	3000pcs per reel
Reel	According to EIA 481-1A specs	Conductive black	
Label	HT standard	Paper	
Packing bag	220x240mm	Aluminum laminated bag/ no-zipper	One reel one bag
Carton	HT standard	Paper	Non-specified

Others:

Every mid-box will be loaded 5 reels. These 5 reels can be different in lot, Iv, lambda, or Vf. Every reel will have an independent label to identify its specification and the mid-box there will have a corresponding label post on it.

ATTENTION: Electric static Discharge (ESD) protection



The symbol shown on the page herein to introduce 'Electro-Optical Characteristics'. ESD protection for GaP and AlGaAs based chips is still necessary even though they are safe in low static-electric discharge. Parts built with AlInGaP,

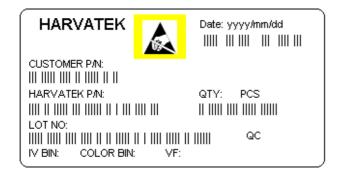
GaN, or/and InGaN based chips are **STATIC SENSITIVE devices**. ESD protection has to considered and taken in the initial design stage.

If manual work/process is needed, please ensure the device is well protected from ESD during all the process.

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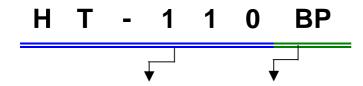


Label Spec.:



■ Customer P/N: To Be Defined





Series Name	Emitting Color
HT-110: 3.2x1.5x1.0mm	BP:
	White

Lot No.

1 2 3 4 5 6 7 8 9 10

P 1 2 2 3 0 A - C T

Code 1	Code 2	Code 3	Code 4, 5	Code 6, 7	Code 9	Code 10
	Mfg. Year	Mfg. Month	Mfg. Date	Lots	Resin Color	Packaging
		1: Jan.				
	Z: 2000	2: Feb.				
Internal	1: 2001			01~99,	C: Water Clear	T: Taped Reel
	2: 2002	9: Sep.	1~31/ (30)			
Tracing Code	3: 2003	A: Oct.		A,B,C		
		B: Nov.				
		C: Dec.				

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Iv Bin:

Color	Bin Code	Spec. Range
	Р	45-72mcd
White	Q	72-112.5mcd
	R	112.5-180mcd

Color Bin:

Color	Bin Code	Spec. Range
	C1	X:0.25~0.27
	Ci	Y:0.2325~0.277
	C2	X:0.27~0.29
\Mbito	C2	Y:0.26~0.305
white	White	X:0.29-0.31
	D1	Y:0.2875-0.3325
	D2	X:0.31-0.33
		Y:0.315-0.36

■ Vf Bin:

Color	Bin Code	Spec. Range
	G8	2.7-2.9V
	H7	2.9-3.1V
Blue	Н8	3.1-3.3V
Diue	J7	3.3-3.5V
	J8	3.5-3.7V
	K7	3.7-3.9V

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Product Feature

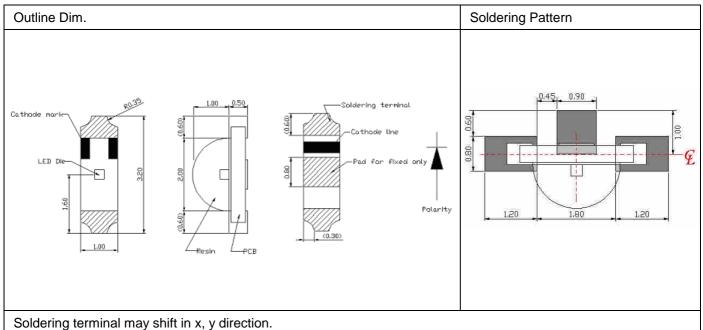
Electro-Optical Characteristics

(IF @ 5mA, Ta 25 °C)

Code for parts Lighting Col	Lighting Color Material -	V _F (V)		λ (nm)	I* _V (mcd)	
	Lighting Color	ivialeriai	typ	max	λ_{D}	Min,
HT-110BP	White	InGaN	3.3	3.9	X:0.29 Y:0.32	18

Package Outline Dimension and Recommended Soldering Pattern for Reflow Soldering

Unit: mm Tolerance: +/-0.1



Absolute Maximum Ratings

(Ta 25 °C)

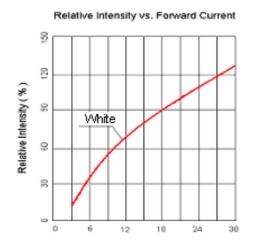
							(14 20 0)
Series	P _d (mW)	I _F (mA)	I _{FP} (mA)	V _R (V)	I _R (uA)	T _{OP} (°C)	T _{ST} (°C)
HT-110BP	78	20	80**	5	<100@ V _R = 5	-30~+80	-40~+85

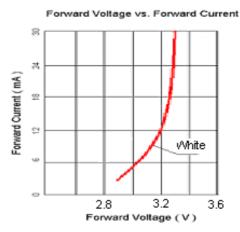
^{**} Condition for I_{FP} is pulse of 1/10 duty and 0.1msec width

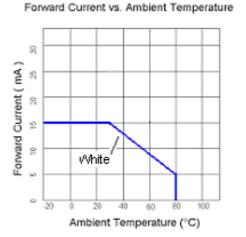
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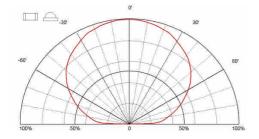
Characteristics of HT-110 Series



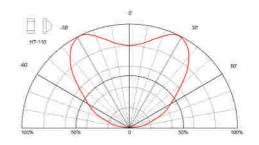




Directive Characteristics



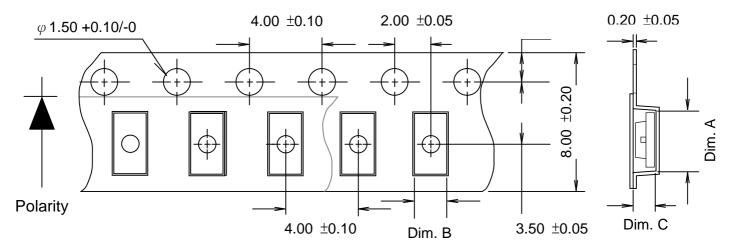
Directive Characteristics



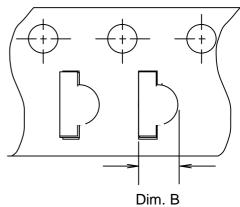
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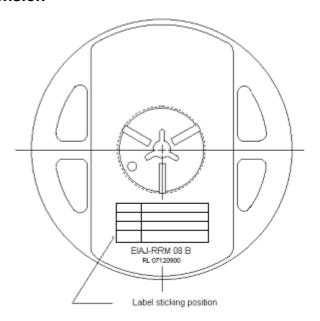
Packaging Tape, Reel, and Packing Model Tape Dimension

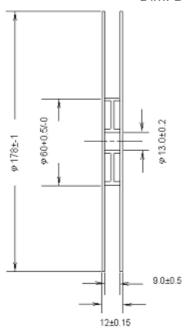


Part No.	Dim. A	Dim. B	Dim. C	Q'ty/Reel
HT-110	3.40±0.10	1.70±0.10	1.20 ±0.10	3K



Reel Dimension

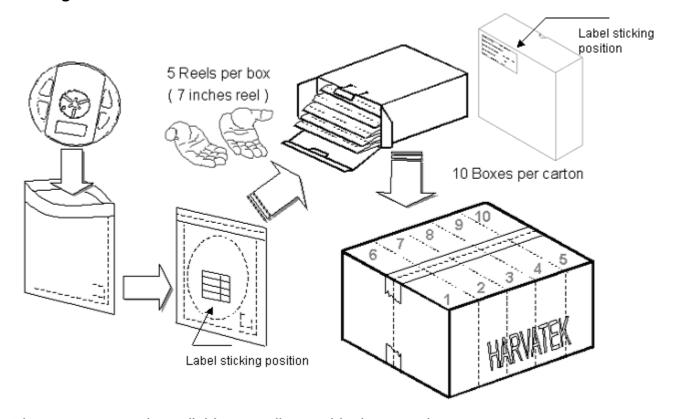




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Packing Model



5 boxes per carton is available according to shipping quantity.

Dry Pack

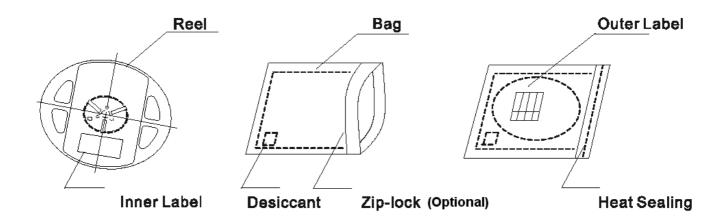
Any SMD optical device, like this chip LED, is **MOISTURE SENSITIVE device**. Avoid absorbing moisture at any time during transportation or storage. Every reel will be packaged in the moisture barrier anti-static bag (Specific bag material will depend upon customers' requirement or option). And the bag is well sealed before shipment.

By customer's requirement, we will put a humidity indicator in each moisture barrier anti-static bag before shipment.

The package is the following:

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Cautions of Pick and Place

It should be avoided to load stress on the resin during high temperature.

Avoid rubbing or scraping the resin by any object.

Electric-static may cause damage to the component. Please confirm that the equipment grounding well. Using an ionizer fan is recommended.

PRECAUTIONS

- 1. Avoid absorbing moisture at any time during transportation or storage.
- Anti-Static process is needed especially when handling GaN, InGaN, and AllnGaP products.
- 3. It is suggested to connect the unit with a proper series current limit resistor. Avoid driving reverse voltage over the specification of LEDs when turning the unit ON/OFF.
- 4. Any application should refer to the specifications of absolute maximum ratings.
- 5. Avoid any direct contact with the viewing area.
- 6. If possible, assemble the unit in a clean room or dust-free environment.

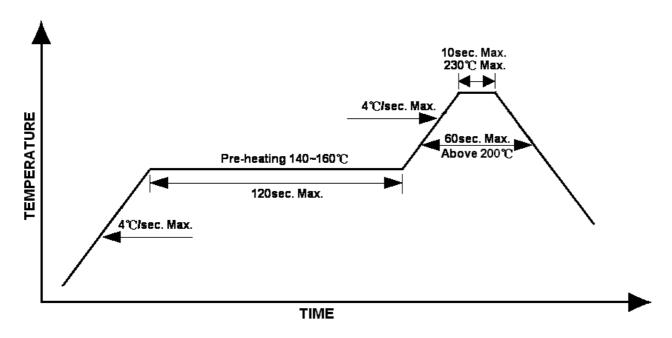
Reflow Soldering

- Recommend tin glue specifications:
 Melting temperature: 178~192 OC
- Never take next process until the component is cooled down to room temperature after reflow.
- ◆ The recommended reflow soldering profile (measuring on the surface of the LED resin) is following:

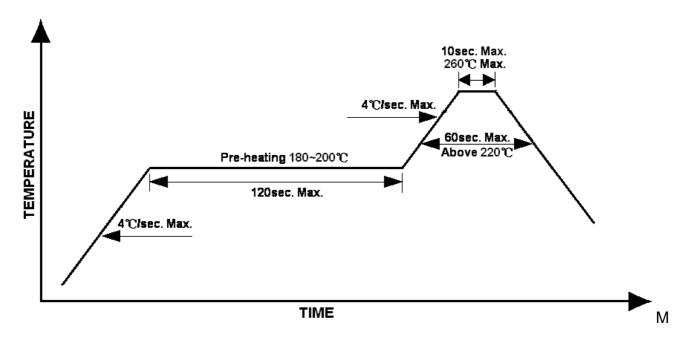
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Lead Solder



Lead-free Solder



Cleaning

The conditions of cleaning after soldering:

An alcohol-based solvent such as isopropyl alcohol (IPA) is recommended.

Temperature×Time: <50 °C×30sec, or <30 °C×3min

Ultra sonic cleaning: < 15W/ bath; Bath volume: 1liter max.

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Curing: 100 °C max, <3min

Do not contact with component on the assembly board.

Cautions of Pick and Place

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Reliability Test

Item	Frequency/ lots/ samples/ failures	Standards Reference	Conditions
Precondition	For all reliability monitoring tests according to JEDEC Level 2	J-STD-020	1.) Baking at 85°C for 24hrs 2.) Moisture storage at 85°C/ 60% R.H. for 168hrs
Solderability	1Q/ 1/ 22/ 0	JESD22-B102-B And CNS-5068	Accelerated aging 155°C/ 24hrs Tinning speed: 2.5±0.5cm/s Tinning: A: 215°C/ 3±1s or B: 260°C/ 10±1s
Resistance to soldering heat		CNS-5067	Dipping soldering terminal only Soldering bath temperature A: 260+/-5°C; 10+/-1s B: 350+/-10°C; 3+/-0.5s
Operating life test	1Q/ 1/ 40/ 0	CNS-11829	1.) Precondition: 85°C baking for 24hrs 85°C/ 60%R.H. for 168hrs 2.) T _{amb} 25°C; I _F =20mA; duration 1000hrs
High humidity, high temperature bias	1Q/ 1/ 45/ 0	JESD-A101-B	T _{amb} : 85°C Humidity: 85% R.H., I _F =5mA Duration: 1000hrs
High temperature bias	1Q/ 1/ 20	HT specs.	T _{amb} : 55°C I _F =20mA Duration: 1000hrs
Pulse life test	1Q/ 1/ 40/ 0		$T_{amb}25$ °C, I_f =20mA,, I_p =100mA, Duty cycle=0.125 (tp=125 μ s,T=1sec) Duration 500hrs)
Temperature cycle	1Q/ 1/ 76/ 0	JESD-A104-A IEC 68-2-14, Nb	A cycle: -40 degree C 15min; +85 degree C 15min Thermal steady within 5 min 300 cycles 2 chamber/ Air-to-air type
High humidity storage test	1Q/ 1/ 40/ 0	CNS-6117	60 <u>+</u> 3°C 90+5/-10% R.H. for 500hrs
High temperature storage test	1Q/ 1/ 40/ 0	CNS-554	100 <u>+</u> 10°C for 500hrs
Low temperature storage test	1Q/ 1/ 40/ 0	CNS-6118	-40 <u>+</u> 5°C for 500hrs

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